**Department of Electrical Engineering**

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| **Faculty Member:** | **Dated:** |
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| **Course/Section:** | **Semester:** |
|  |  |

**Computer Organization and Assembly Language (CS235)**

**Lab #**

**Operating the board (Introduction part 2)**

**Grp no.**

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| **Name** | **Reg. no.** | **Report Marks / 10** | **Viva Marks / 5** | **Total/15** |
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|  |  |  |  |  |

Objective

Upon completion of this lab, you will be able to describe and execute the addressing modes of the 80386 CPU by using the 32 bit MICROPROCESSOR circuit board.

**Answer the following questions.**

# 6-When the base value is in the BP register (see figure 2), what is the default segment for data?

Stack Segment

# 7. The instruction MOV EDX, [DI+BX+0030H] uses what type of addressing mode?

The addressing mode is base index with displacement. Base Address, index and the offset add up to form an effective address.

# 8. What addressing mode can be used only for 32-bit addressing?

Based Scaled Index Addressing mode can be used only for 32-bit addressing.

# 9. For 32-bit addressing using a scaled index, what values can the scale factor have?

The scale factors for scaled index can be 1,2,4 or 8.

10. In the real mode, what instruction code hexadecimal prefix must you use when applying a scaled index in the EA calculation?

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